3. LOGIC GATES

1 MARK QUESTIONS

- 1. What is output of two input XNOR gate for the input A=0,B=1?
- 2. What is truth table?
- 3. What is the output of the two input AND gate for inputs: A = 0 & B = 1.
- 4. What is mean by universal gates?
- 5. What is NOT operator?
- 6. Write the standard symbol for NAND gate.
- 7. Write the truth table and symbol for OR gate.
- 8. GIVE AN EXAMPLE FOR DERIVE GATES.
- 9. Give logic symbol for AND gate

2 MARK QUESTIONS

- 1. Write symbol for NOR gate.
- 2. Explain XOR gate.
- 3. What is mean by dual form? Given an example.
- 4. What is principle of duality? given a example.
- 5. What is mean by proof by perfect induction? Given an example.
- 6. Give logic symbol and truth table for NOR gate.

3 MARK QUESTIONS

- 1. What is meant by proof by perfect induction ? Give an example
- 2. Write the symbol of XOR, XNOR and NAND gates.
- 3. Realize AND, OR & NOT gates suing NOR gate.
- 4. Write the truth table and standard symbol of AND gate.
- 5. Realize AND, OR & NOT gates using NAND gate.